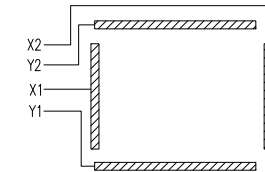
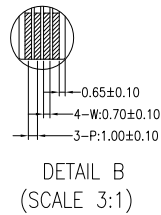
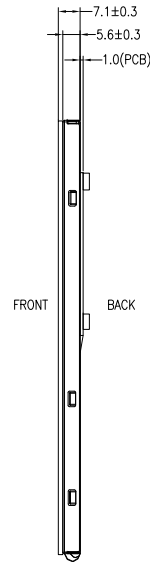
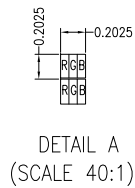
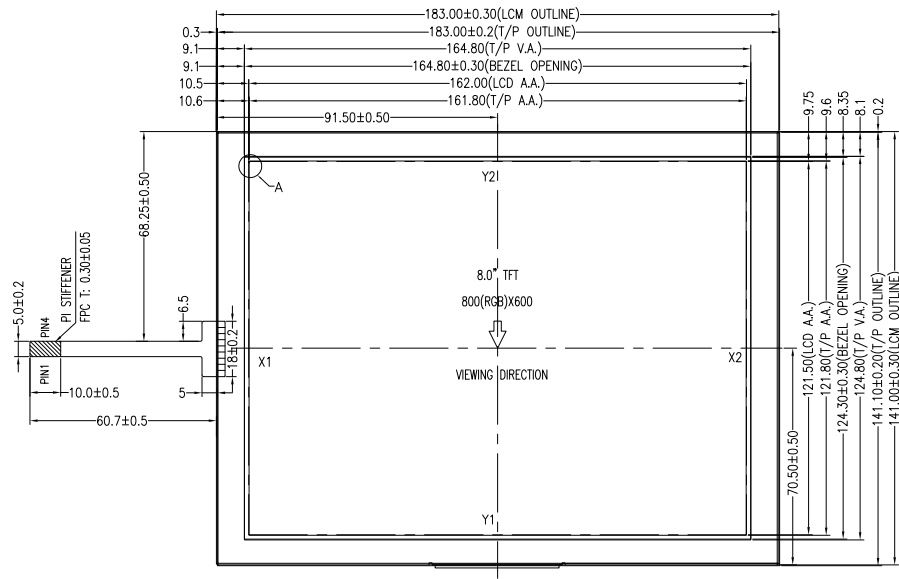


NO MECHANICAL HOUSING TO HOLD THE TFT AND THE TOUCH PANEL ON THIS PRODUCT, SO THE END UNIT'S MECHANICAL DESIGN MUST INCLUDE A HOUSING FOR PREVENTING THE SEPARATION OF THE TFT AND THE TOUCH PANEL.



TOUCH PANEL LOGIC DIAGRAM

NOTES:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY WHITE
2. OPERATING VOLTAGE: VCC=5.0V (TYP.)
3. VIEWING DIRECTION: 12:00 O'CLOCK
4. DRIVER IC: EK79713BCGB + EK73215BCGA
5. OPERATING TEMP.: -10°C ~ 60°C
6. STORAGE TEMP.: -20°C ~ 70°C
7. LED BACKLIGHT: WHITE LED
BUILT-IN BACKLIGHT DRIVER
8. SURFACE LUMINANCE: 225CD/M² (TYP.)
9. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
10. CN2 CONNECTOR: A1007WR-S-20P OR EQUIVALENT
MATCHING CONNECTOR: A1007H-20P OR EQUIVALENT
11. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
12. GENERAL TOLERANCE: ±0.3MM
13. ROHS COMPLIANT

PIN ASSIGNMENT FOR CN2	
PIN	SYMBOL
1	ON/OFF
2	NC
3	NC
4	NC
5	VCC
6	VCC
7	VCC
8	GND
9	RXCLK+
10	RXCLK-
11	RXIN2+
12	RXIN2-
13	RXIN1+
14	RXIN1-
15	RXIN0+
16	RXIN0-
17	NC
18	NC
19	GND
20	GND
PIN ASSIGNMENT FOR RTP	
PIN	SYMBOL
1	Y1
2	X1
3	Y2
4	X2

DRAWN BY: RITA		2024.01.25	MODULE P/N: MIO800F1T-8RP1-B	
CHECKED BY:			SCALE : 1/1	
APPROVED BY:			UNIT: mm	
VER. 01		REVISOR	SHEET NO.: 1/1	
FIRST ISSUE		DATE	DESCRIPTION: TFT +RTP + PCBA MODULE	